# 9 — Schematics

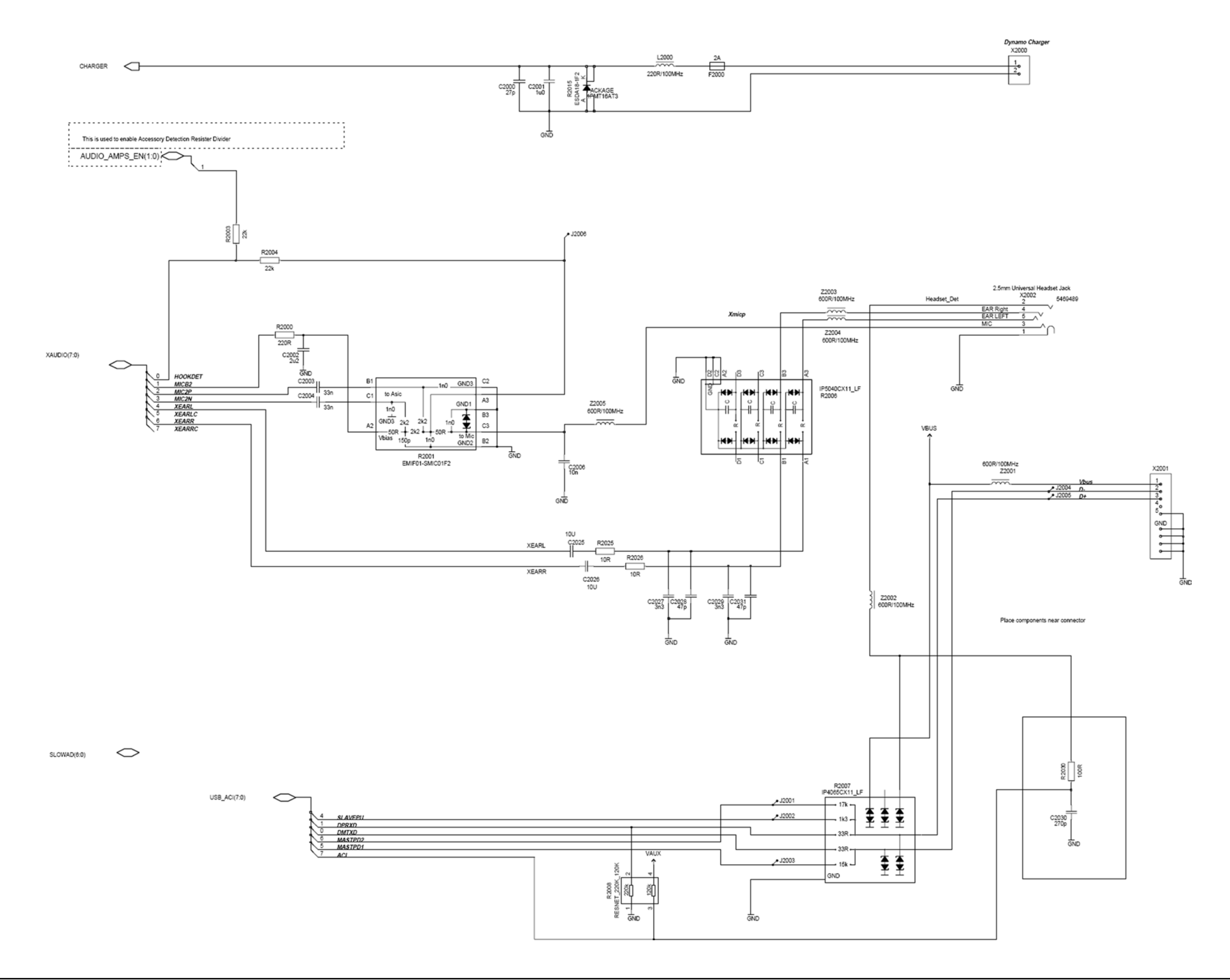
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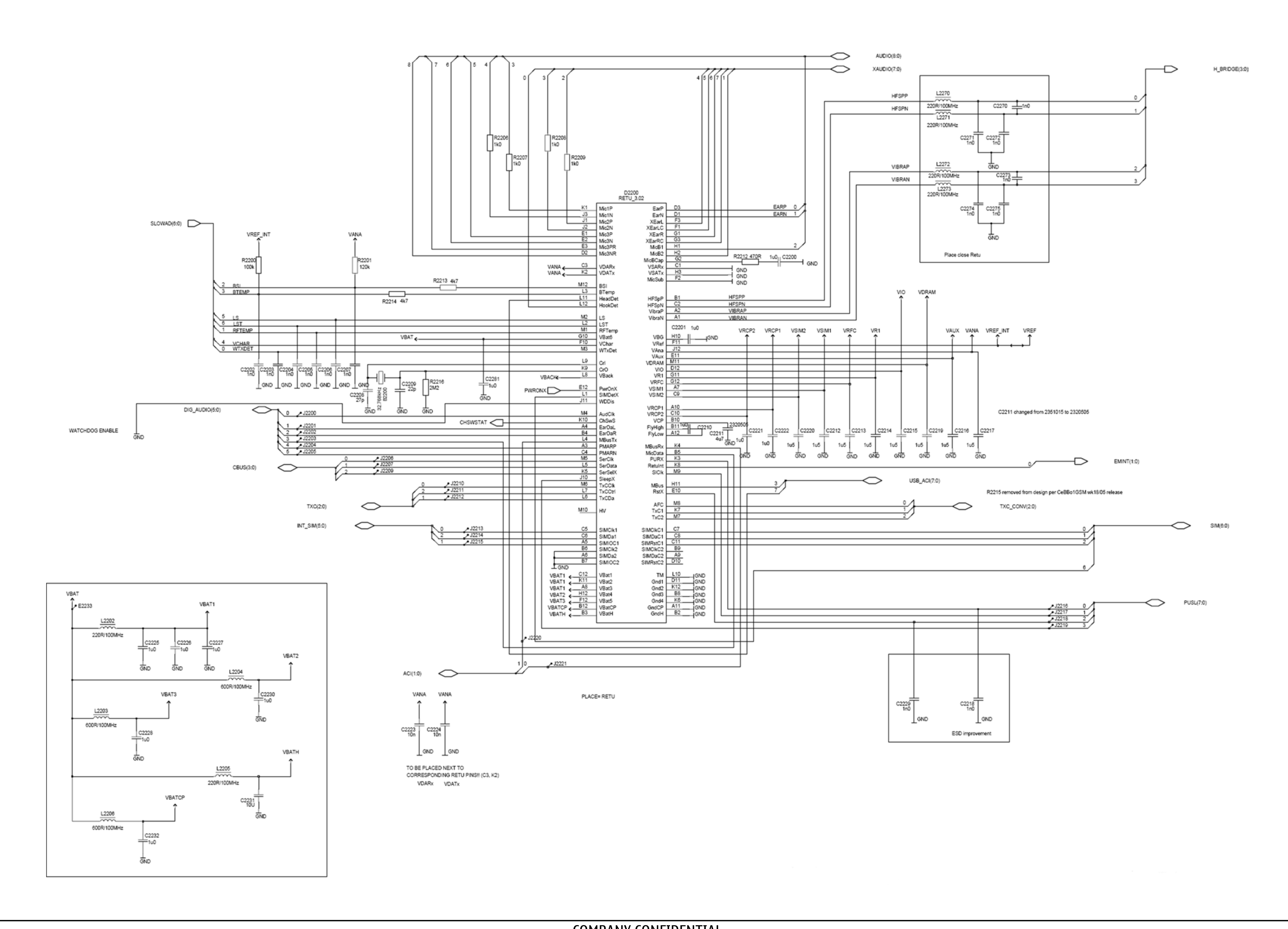


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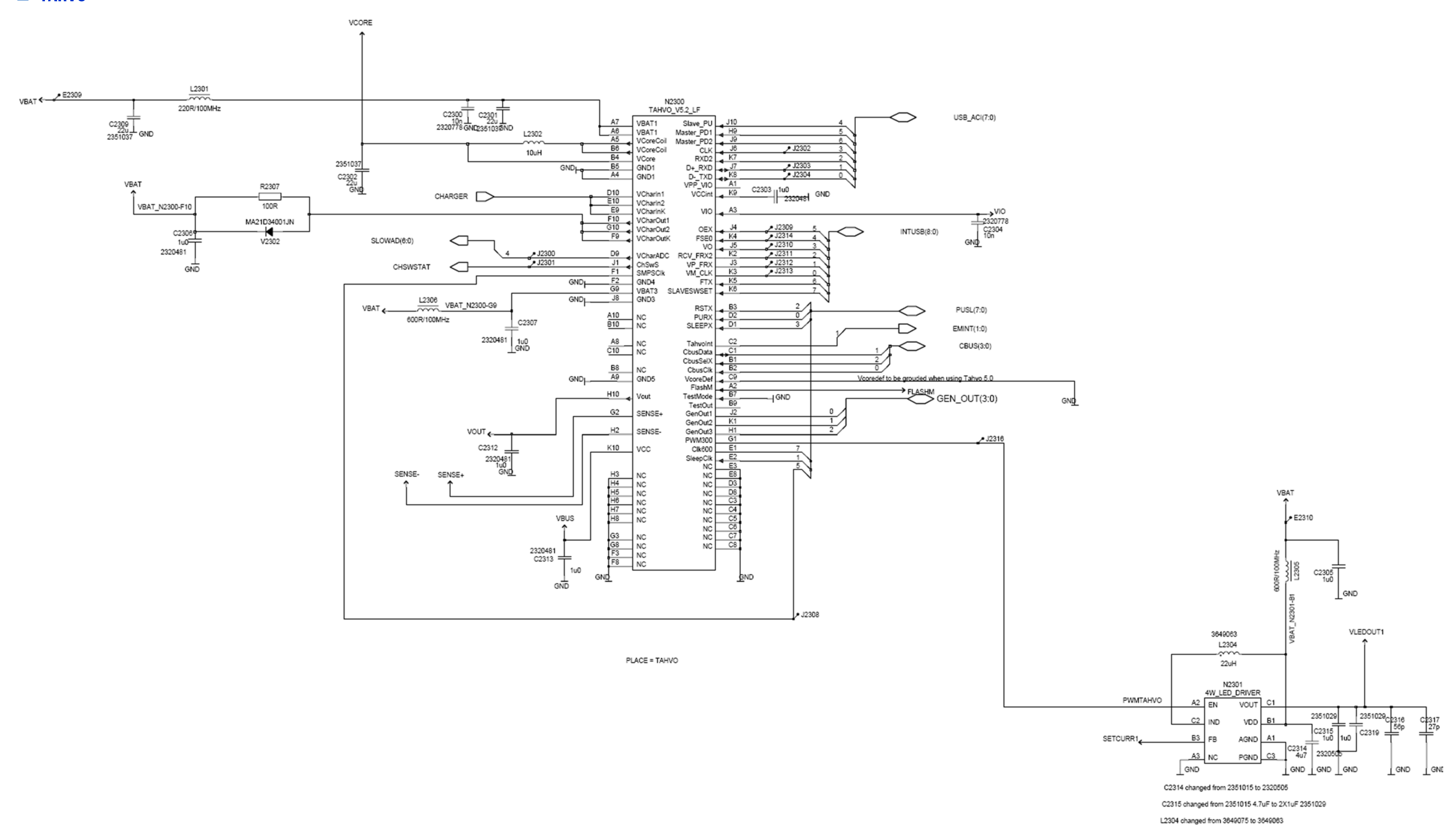
## System connector



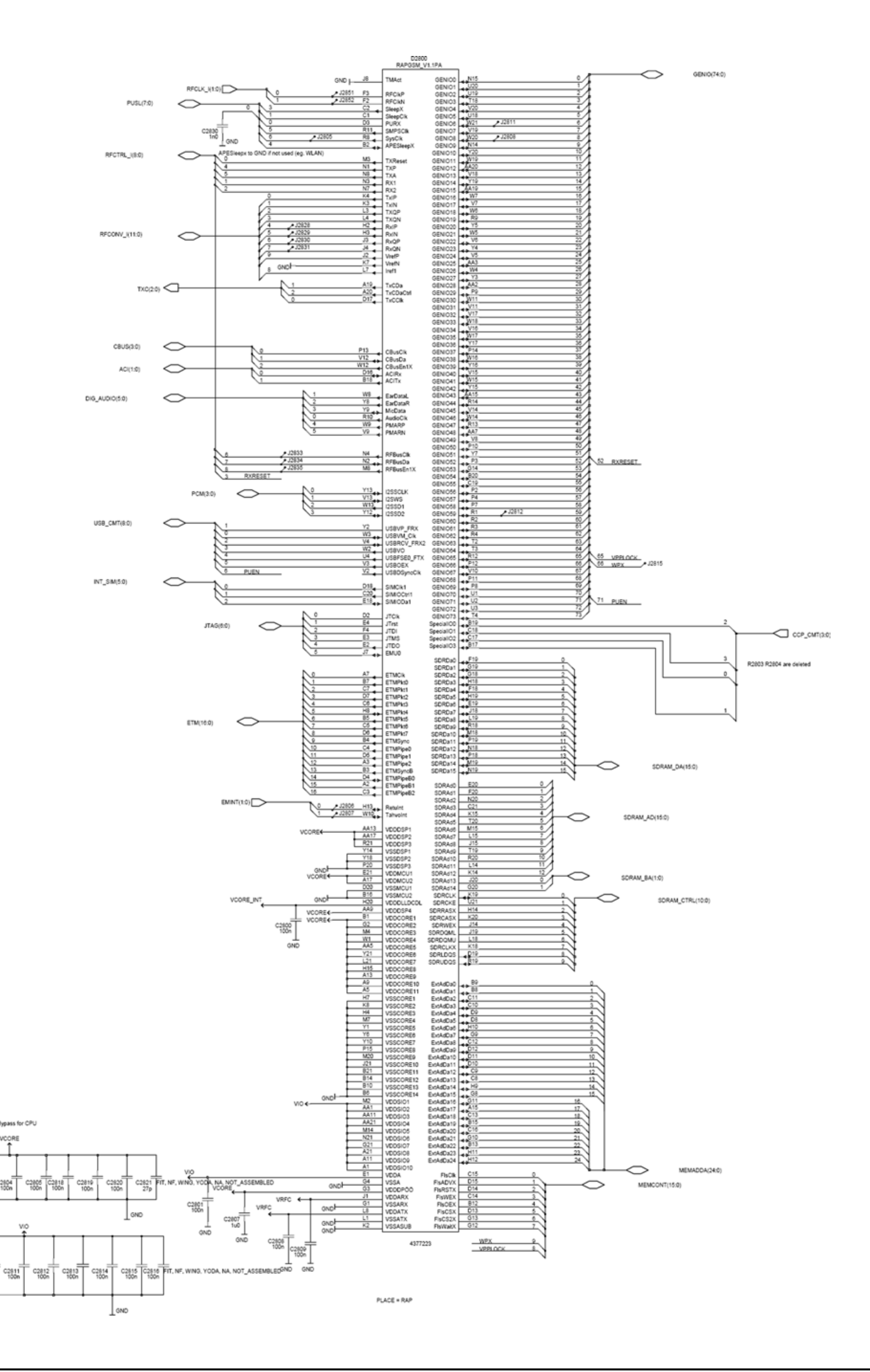
## RETU



## TAHVO


VCORE
VBAT
E2309
L2301
220R/100MHz
C2309
22u
2351037
GND
C2300
10n
2320778
C2301
22u
2351038
L2302
10uH
2351037
C2302
22u
N2300
TAHVO_V5.2_LF
VBAT1
VCoreCoil
VCore
GND1
VCharIn1
VCharIn2
VCharInK
VCharOut1
VCharOut2
VCharOutK
VCharADC
ChSwS
SMPSClk
GND4
VBAT3
GND3
NC
GND5
Vout
SENSE+
SENSE-
VCC
Slave_PU
Master_PD1
Master_PD2
CLK
RXD2
D+_RXD
D-_TXD
VPP_VIO
VCCint
VIO
OEX
FSE0
VO
RCV_FRX2
VP_FRX
VM_CLK
FTX
SLAVESWSET
RSTX
PURX
SLEEPX
TahvoInt
CbusData
CbusSelX
CbusClk
VcoreDef
FlashM
TestMode
TestOut
GenOut1
GenOut2
GenOut3
PWM300
Clk600
SleepClk
USB_ACI(7:0)
J2302
J2303
J2304
C2303
1u0
2320481
INTUSB(8:0)
J2309
J2314
J2310
J2311
J2312
J2313
C2304
10n
PUSL(7:0)
EMINT(1:0)
CBUS(3:0)
Vcoredef to be grouded when using Tahvo 5.0
FLASHM
GEN_OUT(3:0)
J2316
VBAT_N2300-F10
R2307
100R
MA21D34001JN
V2302
C2306
1u0
2320481
CHARGER
SLOWAD(6:0)
CHSWSTAT
J2300
J2301
L2306
VBAT_N2300-G9
600R/100MHz
C2307
VOUT
C2312
SENSE-
SENSE+
VBUS
C2313
J2308
PLACE = TAHVO
E2310
L2305
VBAT_N2301-B1
C2305
3649063
L2304
22uH
VLEDOUT1
N2301
4W_LED_DRIVER
PWMTAHVO
SETCURR1
EN
IND
FB
NC
VOUT
VDD
AGND
PGND
2351029
C2315
C2319
C2314
4u7
2320505
C2316
56p
C2317
27p
C2314 changed from 2351015 to 2320505
C2315 changed from 2351015 4.7uF to 2X1uF 2351029
L2304 changed from 3649075 to 3649063

## RAPGSM



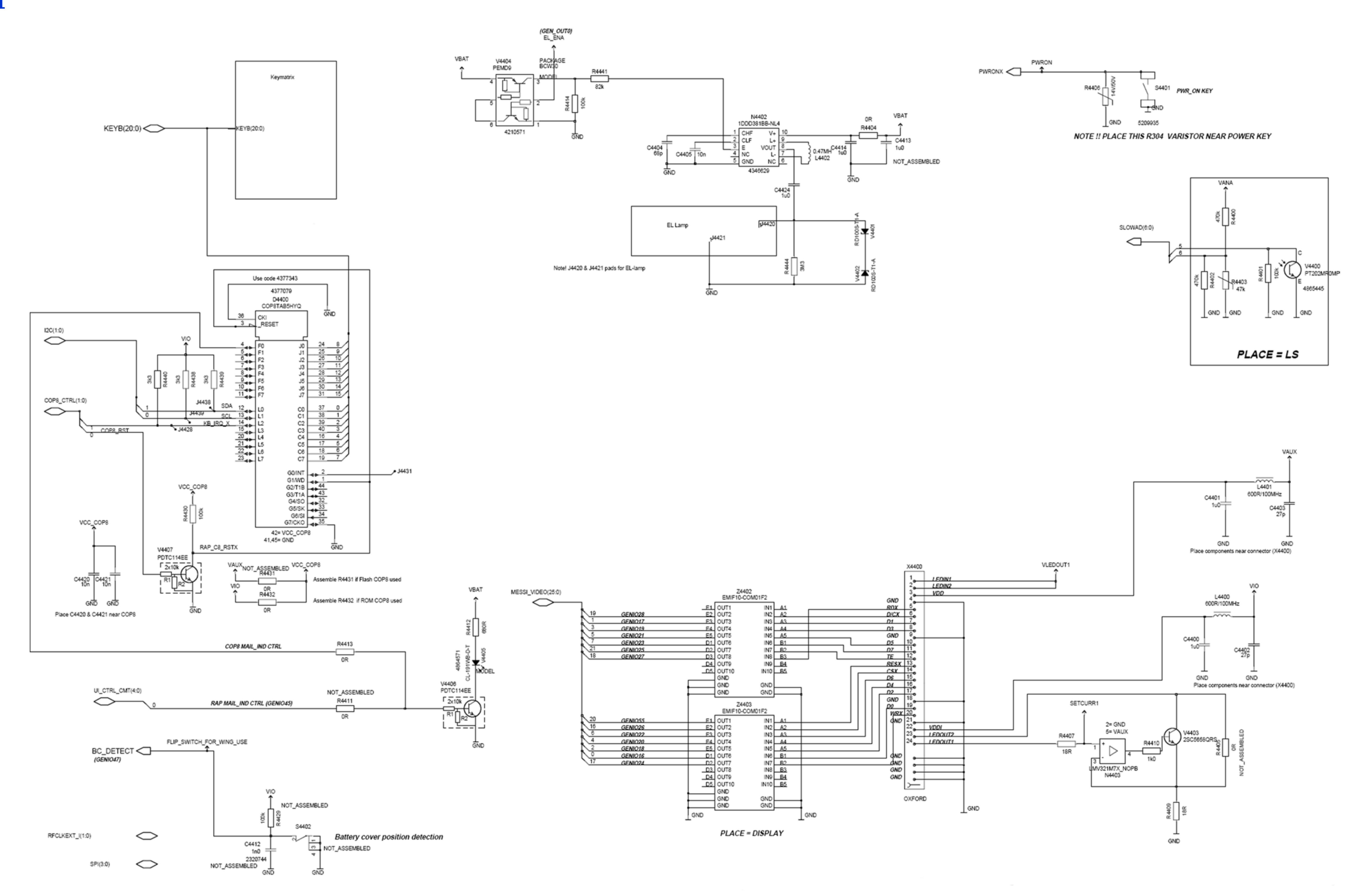




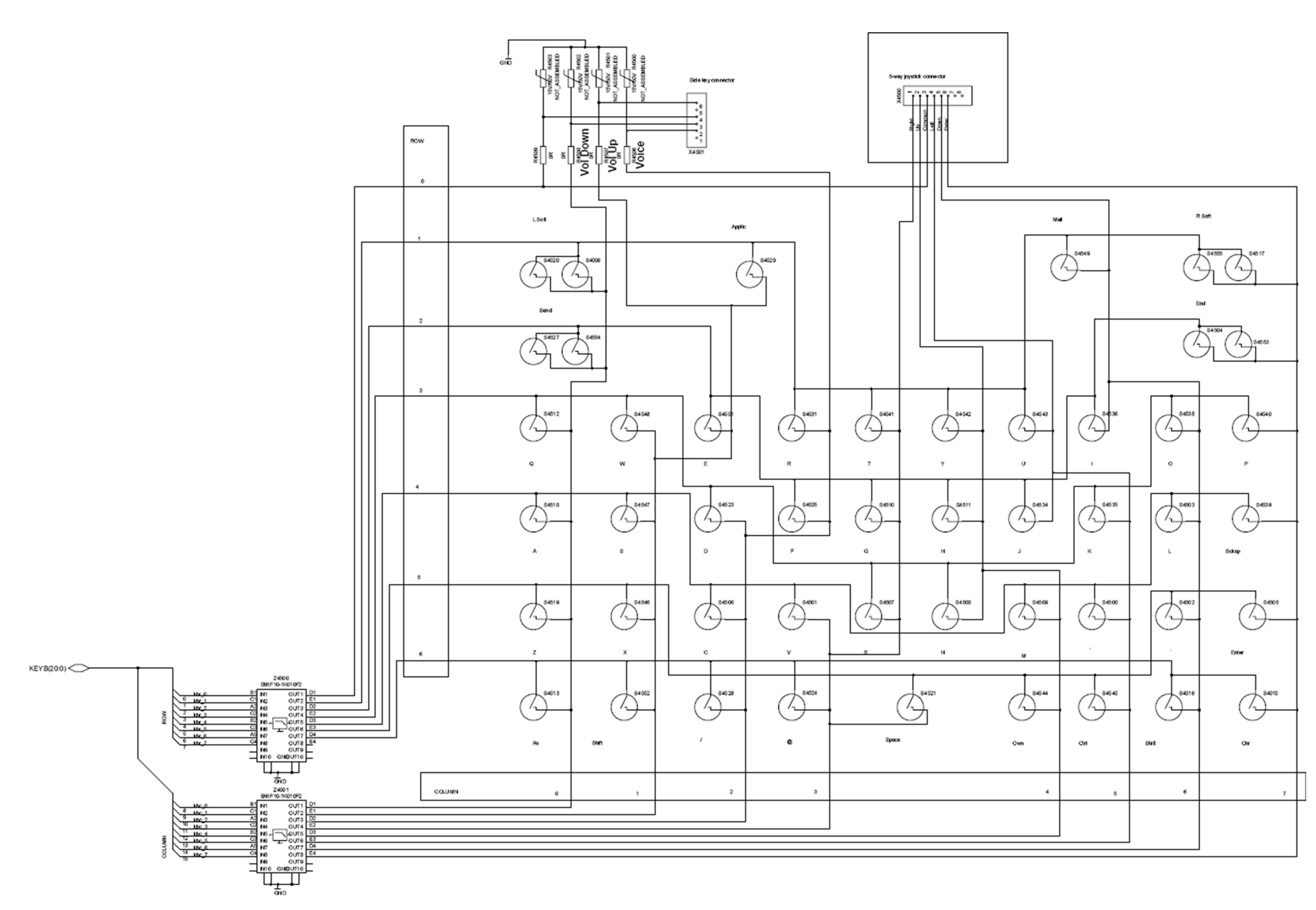
## Bluetooth

BT_RF
antenna
RFCLK_EXT(1:0)
38.4 MHz
PUSL(7:0)
PUSL2(3:0)
R7590 1k8
LPRF(6:0)
J6050
J6051
J6053
bt module reset
uart output from bt module
uart input to bt module
wakeup to bt module
wakeup to RAP
PCM(3:0)
J6054
J6055
J6056
J6057
pcm clk
pcm sync
pcm input to bt module
pcm output from bt module
WLAN_BT(6:0)
WLAN clock request
G_LPRF(23:0)
VR1
R7586 330R
R7592 27k
C7530 4n7
C7590 10n
C7597 100p
V7590 BFR93AW
R7587 0R
R7595 1k2
R7594 1k2
NOT_ASSEMBLED
GND
BT_PERF_2_3_03
LPRFCLK
PUSL(3:0)
G_LPRF(23:0)
ANT_BTH
BC4_TEST(3:0)
SPI(3:0)
BT WLAN RF
A6001
PWB_CAN_WCDMA
WCDMA
9590822
VBAT
VBAT_BTH
C6050 1u0
VIO
VIO_1V8_BTH

## UI part 1



## UI part 2







## RF part

SYS
schematic

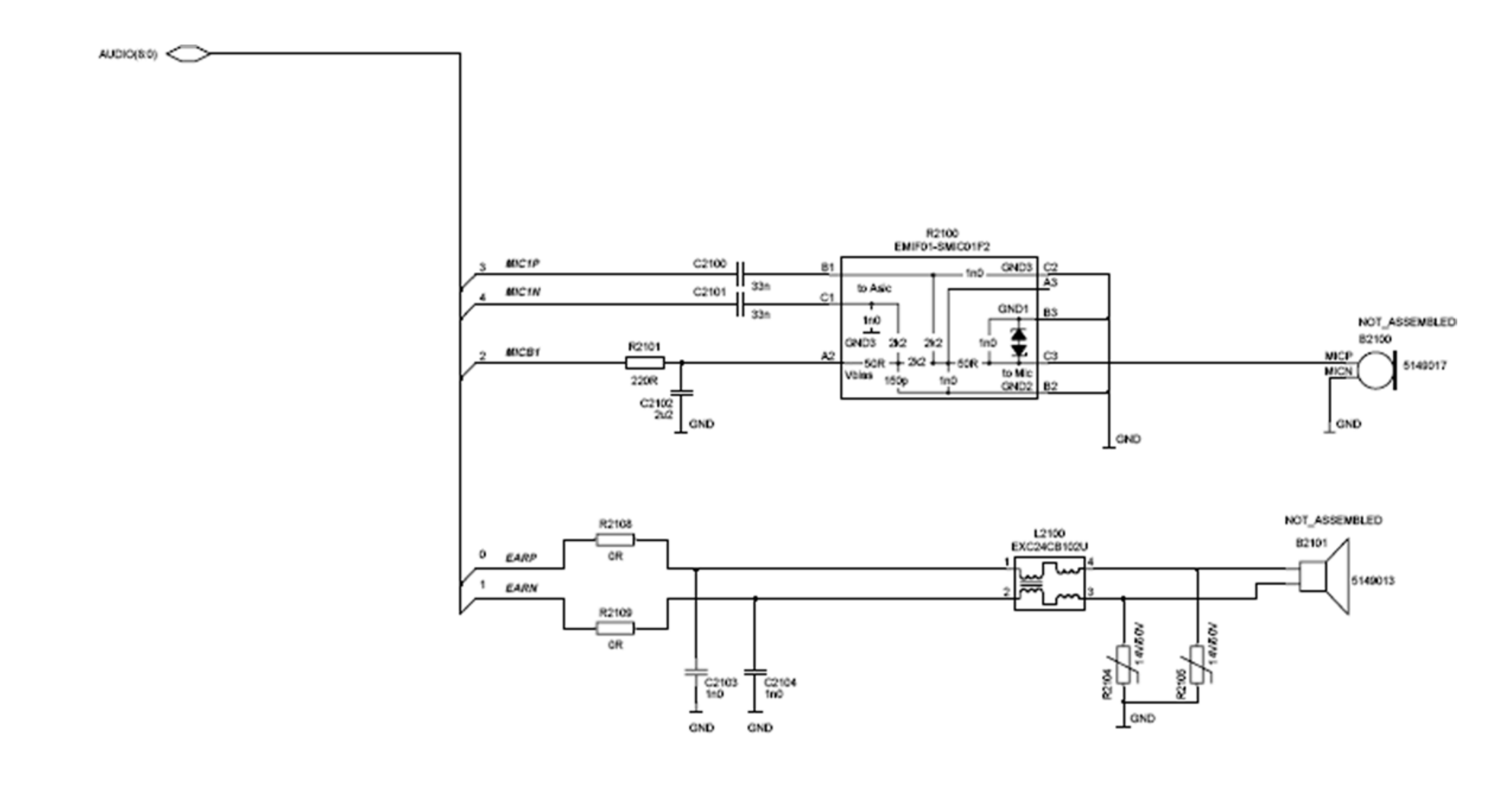
RFPWR(5:0)
RFCTRL(8:0)
RFCONV(11:0)
SLOWAD(6:0)
TXCCONV(2:0)
RFCLK(1:0)
LPRFCLK

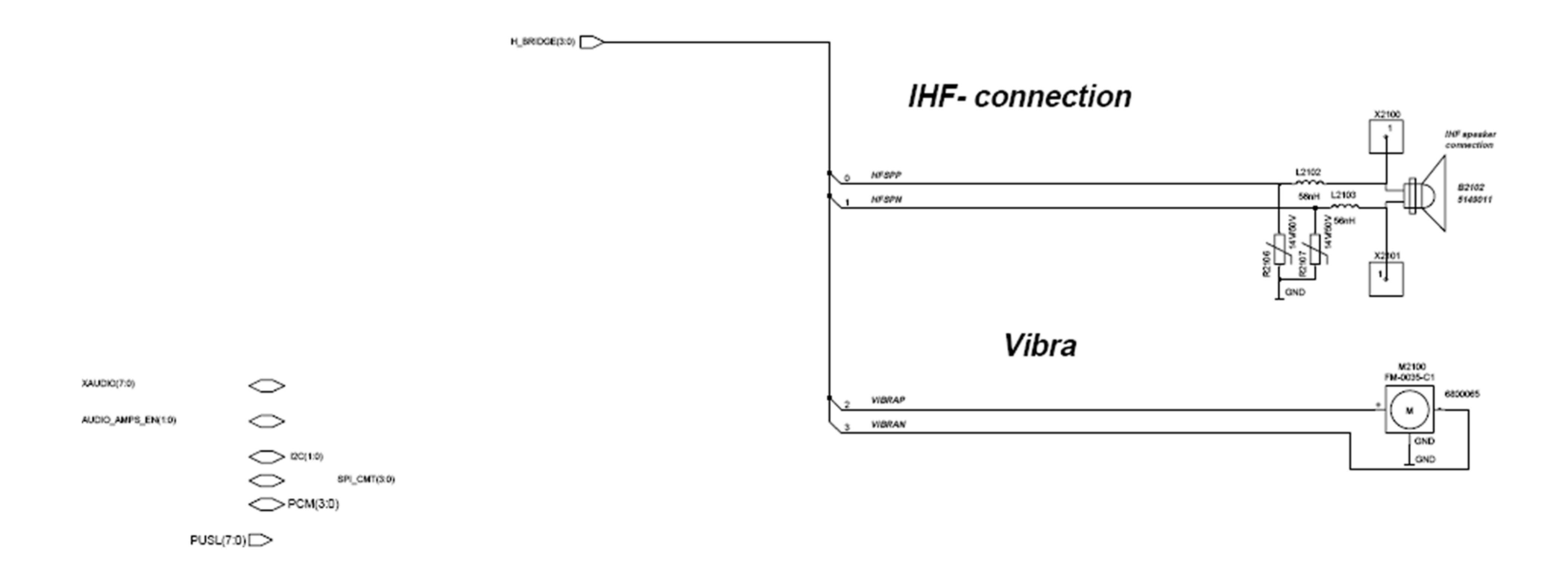
RXIP
RXIN
RXQP
RXQN
VBATRF
VCP1
VCP2
VXO
VREFRF01
VREFRF01
RXresetX
TXresetX
RFBusClk
RFBusDa
RFBusEn1X
AUXDAC1
AFCOUT
TXIP
TXIN
TXQP
TXQN
RFTEMP
TXPWRDET
IREF1

RF

| | |
|---|---|
| VBAT | |
| VCP2 | |
| VXO | |
| TXRESETX | |
| RFBUSCLK | DAC_REF1 |
| RFBUSDAT | RXIP |
| RFBUSENA | RXIN |
| TXQP | RXQP |
| TXQN | RXQN |
| TXIP | VREFCM |
| TXIN | RFCLKEXT |
| TXC | RFCLKN |
| VREFRF01 | RFCLKP |
| VCP1 | WTXDET |
| AFC | RFTEMP |

## Audio, IHF, Vibra







## SIM interface

VSIM1

R2700
EMIF03-SIM02F2
SIM Reader
Side
CLK
R1
R2
R3
CLK
GND
4129257

1 J2712
0 J2710
2 J2711
6
SIMDetX

SIM(6:0)

X2701
M-SK_200300383
1 C1
2 C2
3 C3
C5 5
C6 6
C7 7
5469419

GND

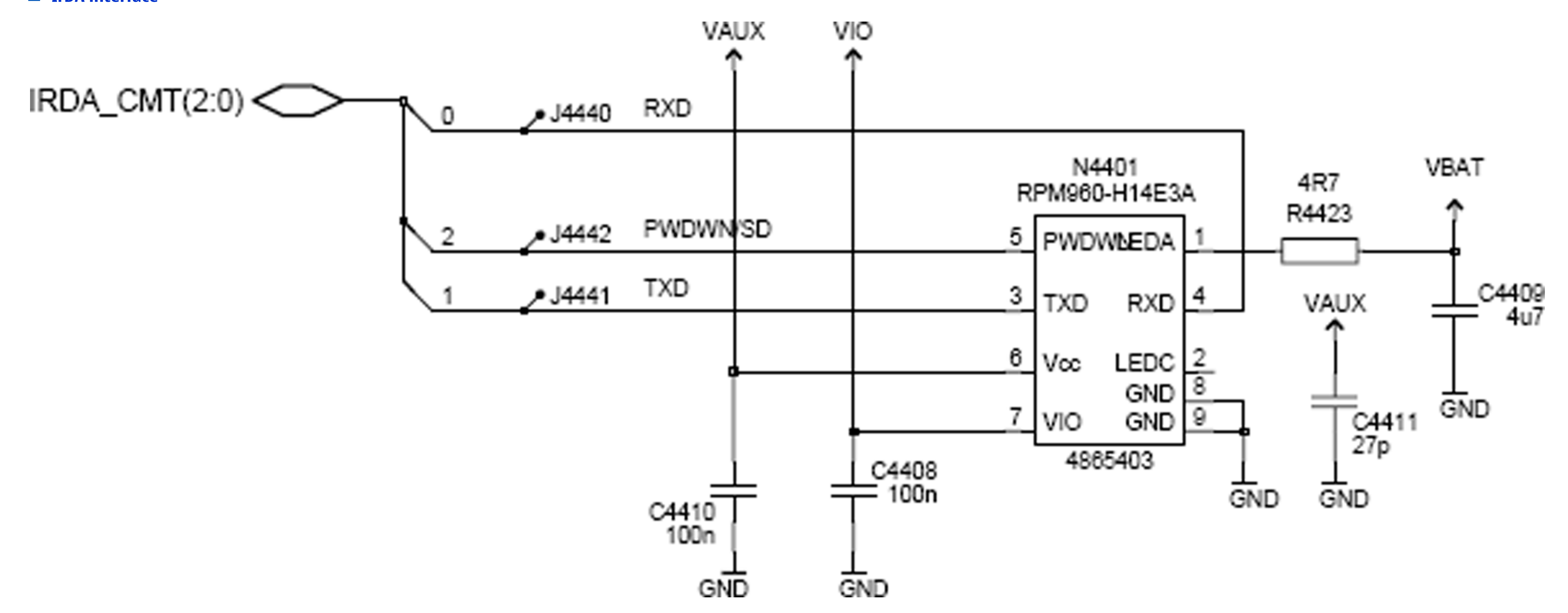
Place = SIM

C2700
100n

GND
GND


COMPANY CONFIDENTIAL

## IrDA interface







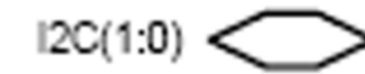
## MMC interface

## CMT memories

## Camera

UI_CTRL_CMT(4:0)






CCP_CMT(3:0)

CAM_CTRL_CMT(4:0)



